



Digital Integrated Circuits

Second Edition

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Problems and Exercises

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Click on the corresponding header to download the exercises covering the different chapters. New problems will be added on a regular basis. So check the update date marked next to the chapter. While solving exercises that require the use of SPICE you can use the models provided in the [models](#) page.

- **Chapter 1: Introduction** ([pdf](#)) - 01/29/03
- **Chapter 2: The Manufacturing Process** (pdf) - Not yet available
- **Chapter 3: The Devices** ([pdf](#)) - 09/01/02
- **Chapter 4: The Wire** ([pdf](#)) - 01/29/03
- **Chapter 5: The CMOS inverter** ([pdf](#)) - Last updated 2/5/03
- **Chapter 6: Designing Combinational Logic Gates in CMOS** ([pdf](#)) - 10/31/02
- **Chapter 7: Sequential Circuits** ([pdf](#)) - 10/30/03
- **Chapter 8: Designing Complex Digital Integrated Circuits** (pdf) - Not yet available
- **Chapter 9: Coping with Interconnect** (pdf) - Not yet available
- **Chapter 10: Timing Issues in Digital Circuits** ([pdf](#)) - 01/21/03
- **Chapter 11: Designing Arithmetic Building** ([pdf](#)) - 09/30/03
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